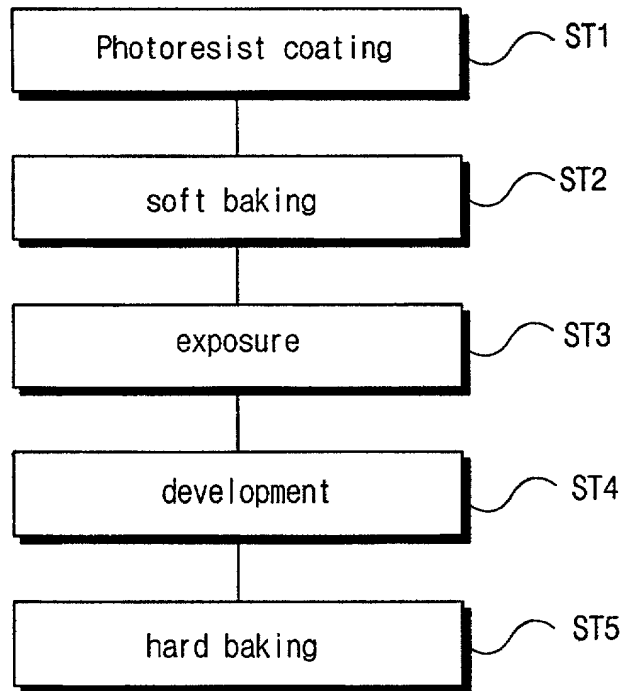
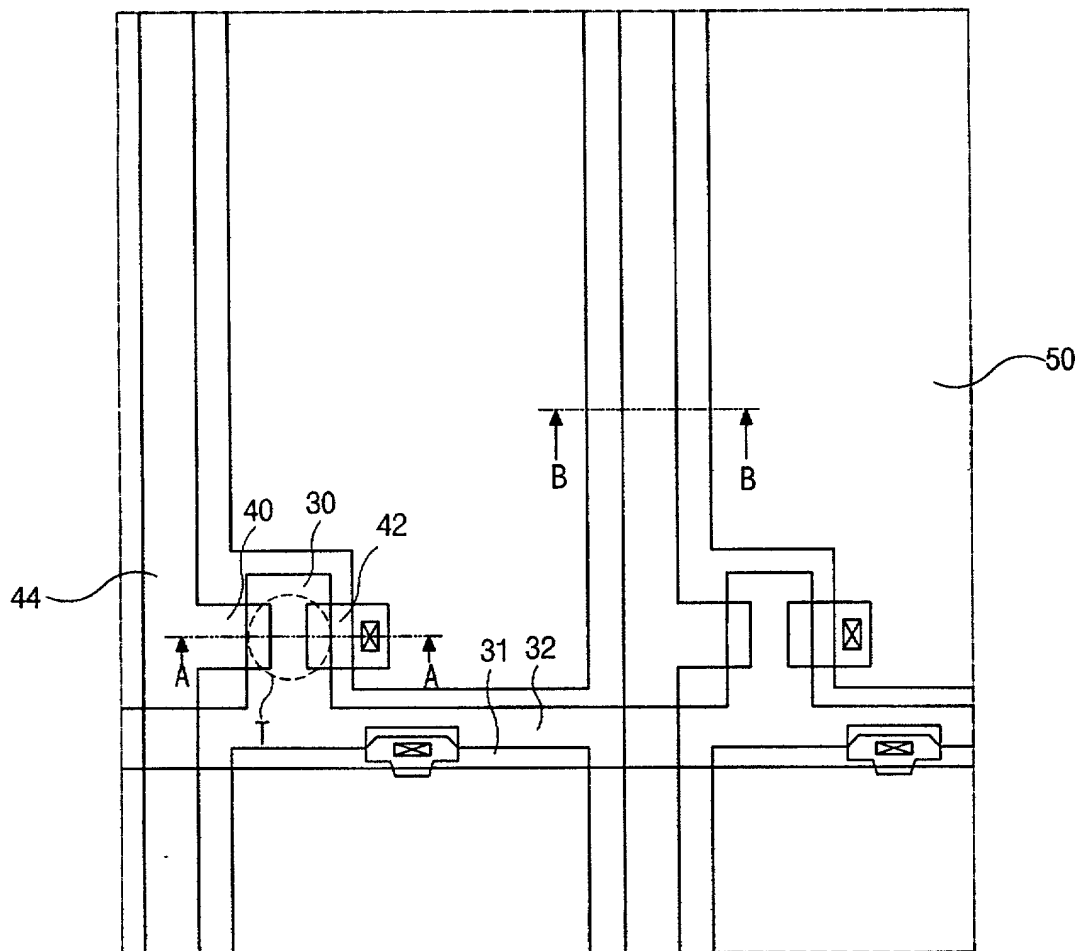


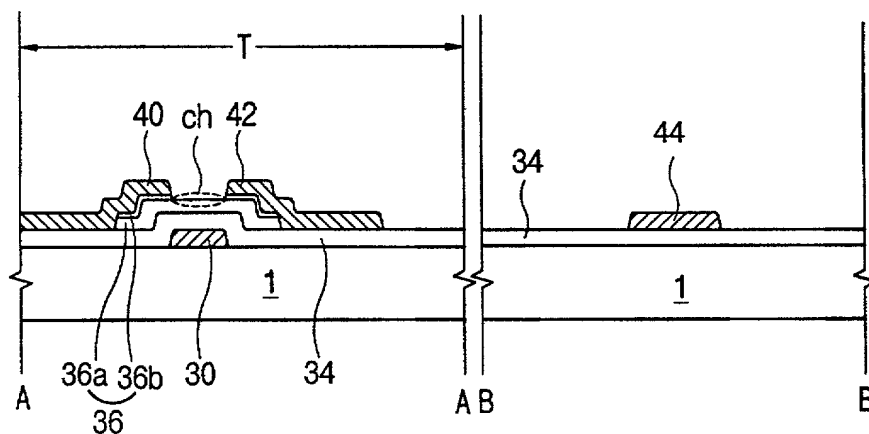
**FIG. 1**  
**(RELATED ART)**



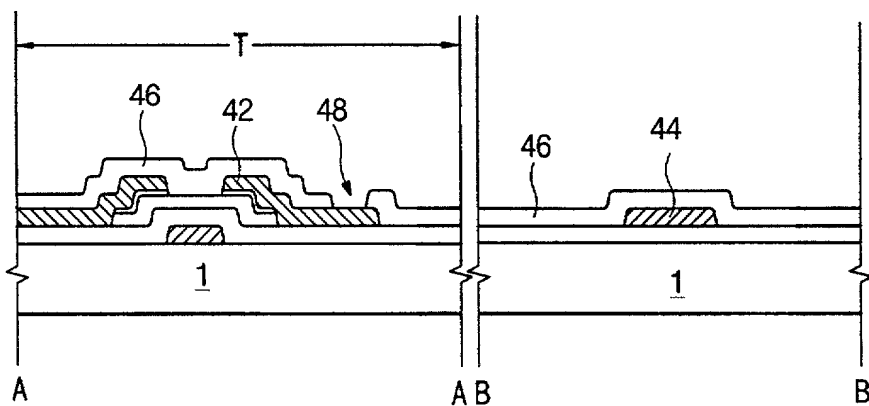
**FIG. 2**  
**(RELATED ART)**



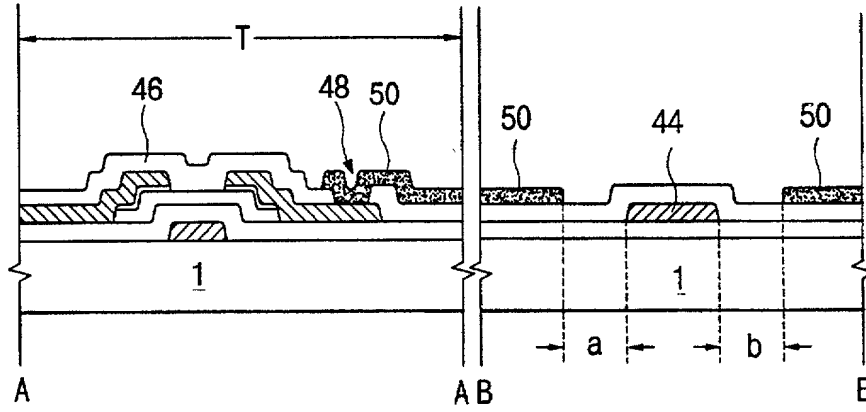
**FIG. 3**  
**(RELATED ART)**



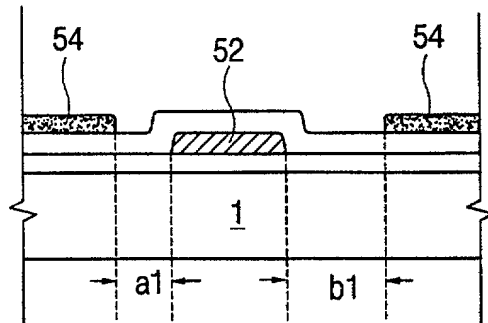
**FIG. 4A**  
**(RELATED ART)**



**FIG. 4B**  
**(RELATED ART)**



**FIG. 4C**  
**(RELATED ART)**



**FIG. 5**  
**(RELATED ART)**

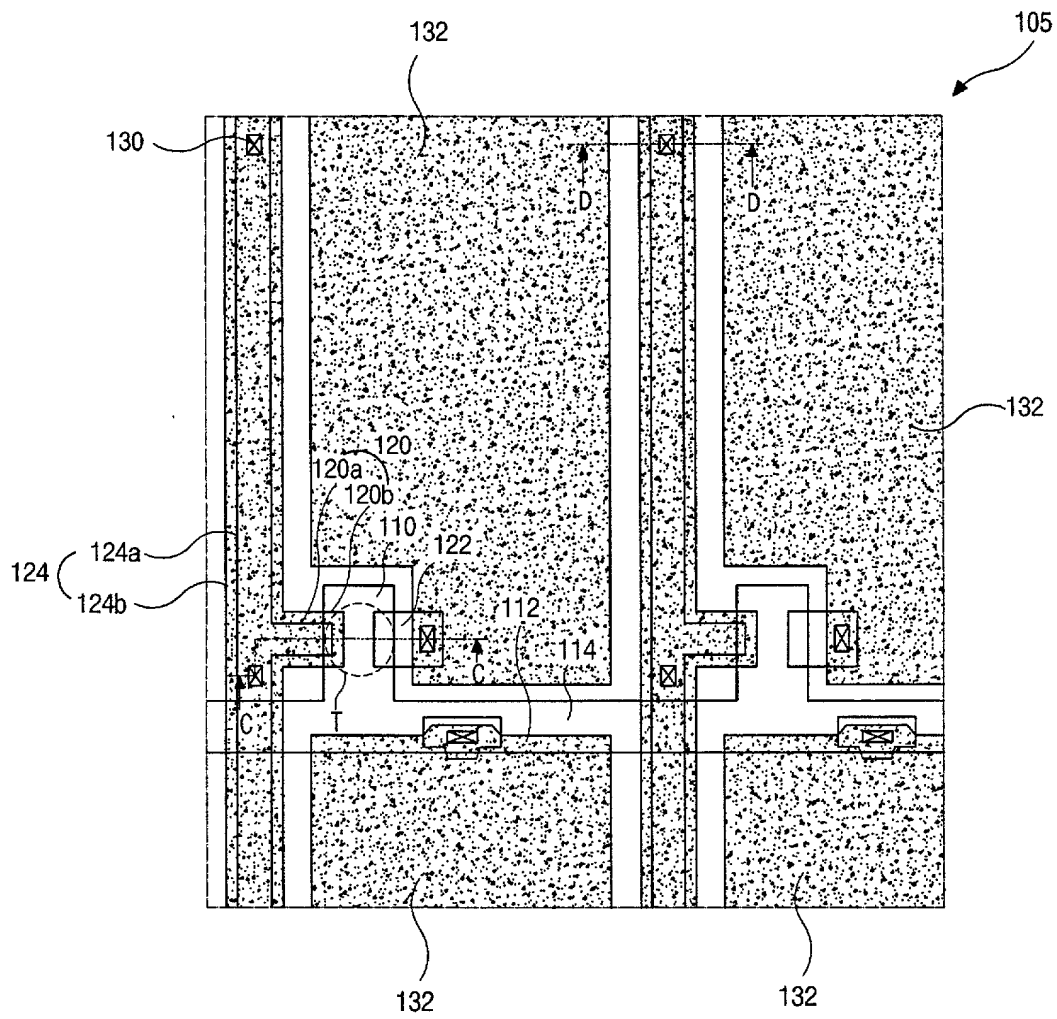


FIG. 6

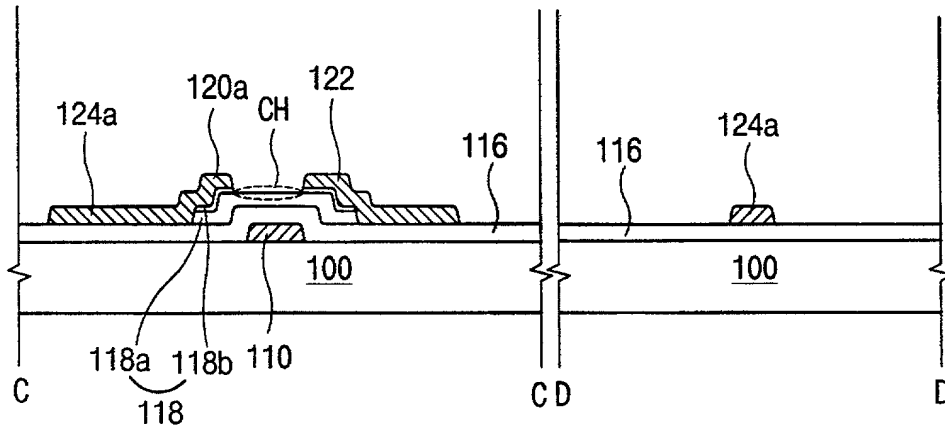


FIG. 7A

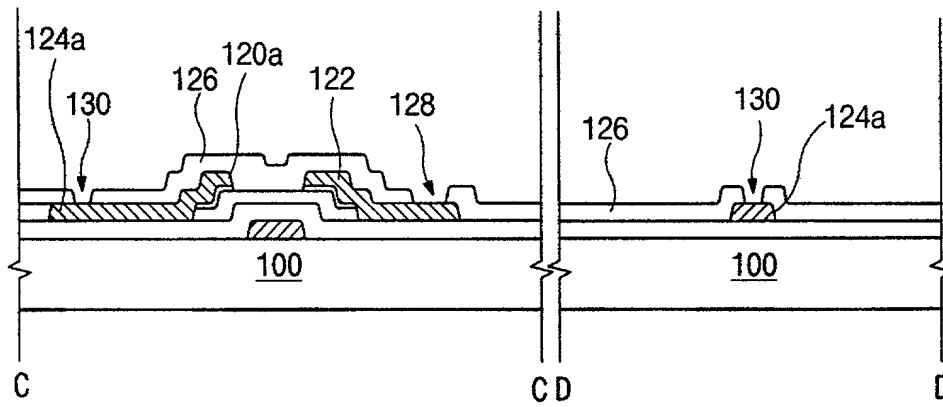


FIG. 7B

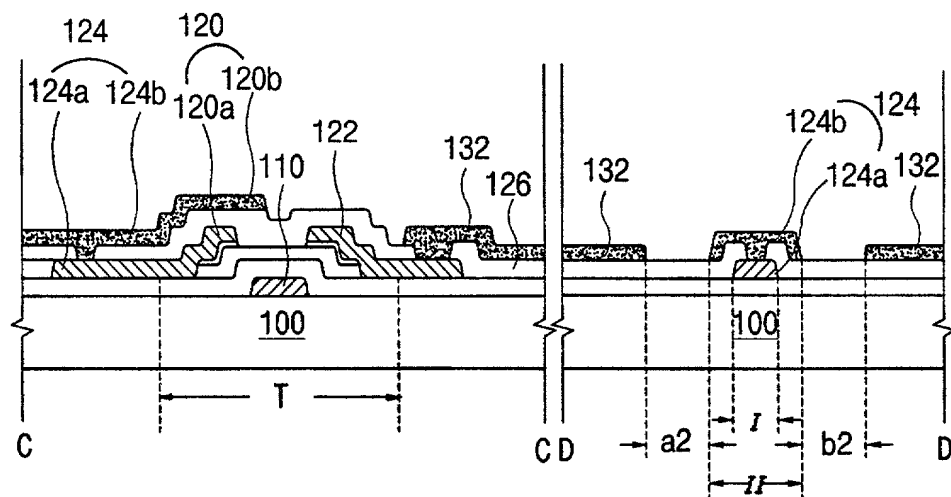


FIG. 7C

FIG. 7C is a cross-sectional diagram of a semiconductor device structure.